

AMENDMENTS TO THE DRAWINGS

The attached sheets of drawings include proposed drawing changes to Figures 1, 5A and 6.



Local and Global Register Partitioning in VLIW Processor

Marc Tremblay and William Joy

09/204,585

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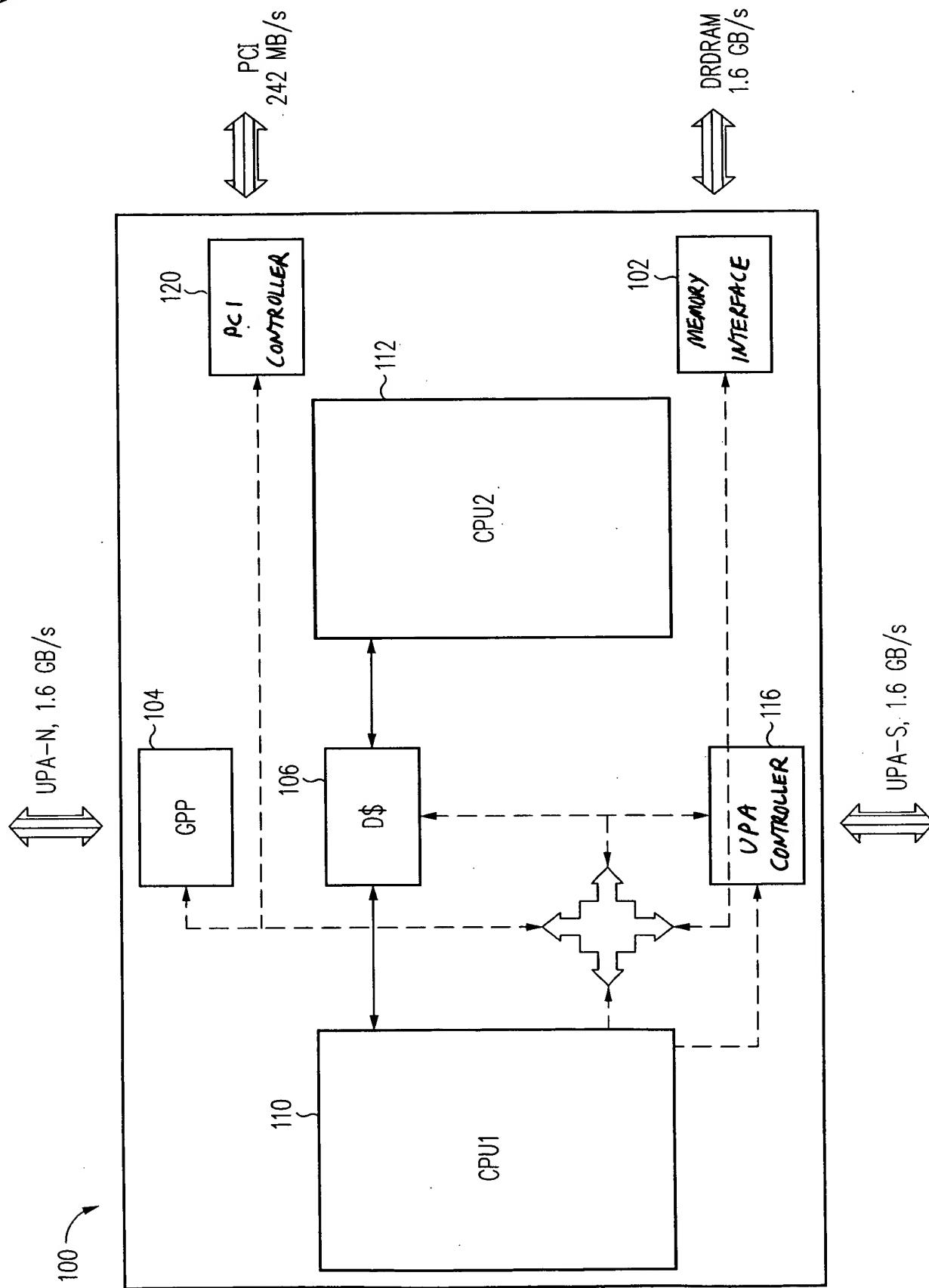


FIG. 1

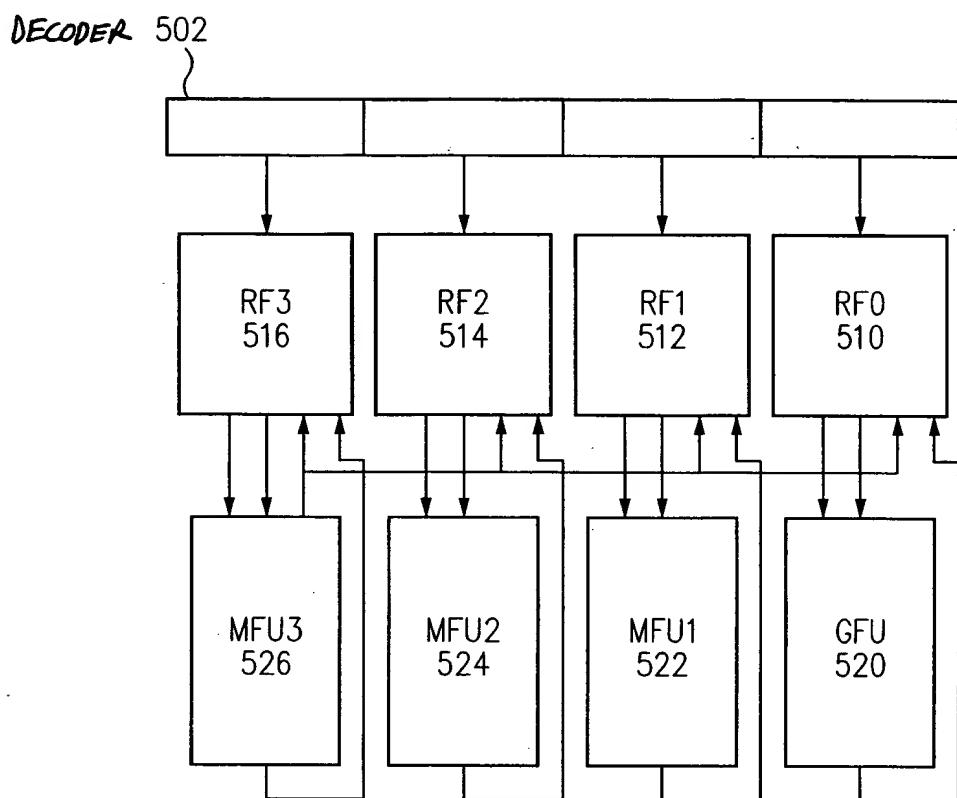


FIG. 5A

muladd Ra, Rb, Rc, Rd

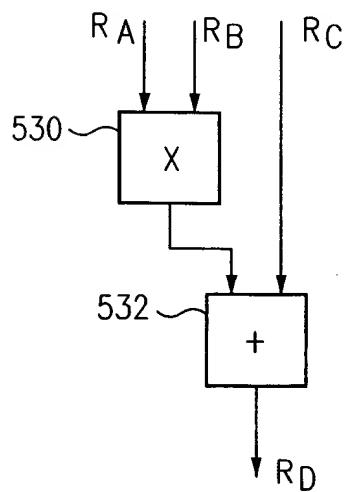


FIG. 5B



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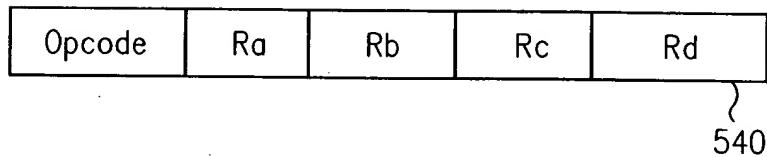


FIG. 5C

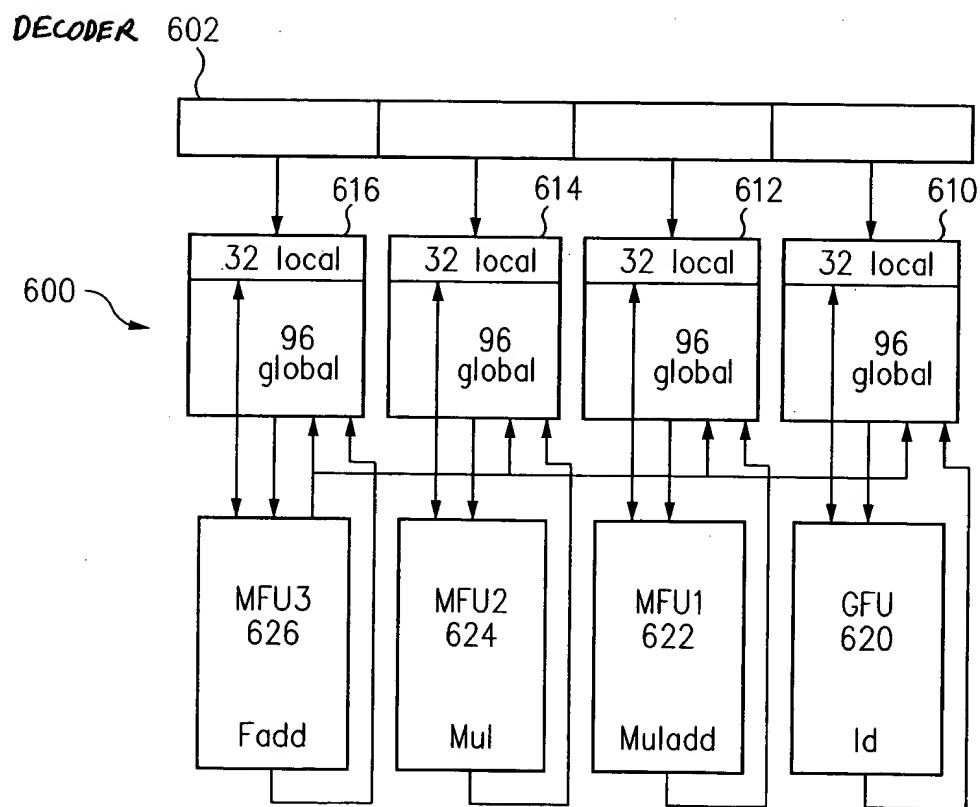


FIG. 6